ANALOG DECODING METHOD AND CORE

ABSTRACT OF THE DISCLOSURE

An analog decoder includes a controller and an analog decoder core. The analog decoder core has an input of an input length that is less than a code length of the code data. If only a single analog decoder core is implemented, then the controller applies a portion of the code data to the analog decoder core prior to each of a plurality of read operations in which decoded data is read from the analog decoder core. Alternatively, if a plurality of analog decoder cores is implemented, then the controller may apply a portion of code data onto each of the analog decoder cores prior to a single read operation associated with each of the analog decoder cores.